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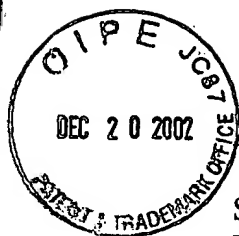
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

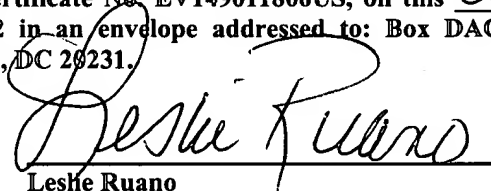
#5

Applicant: Blackham et al. For: APPARATUS USING
Serial No.: 09/881,478 Examiner: FRAMESTORE DEMULTIPLEXY
Filing Date: June 14, 2001 Docket No.: GJ-223J

Box DAC
Commissioner for Patents
Washington, DC 20231

CERTIFICATE OF EXPRESS MAILING

I hereby certify that the attached correspondence is being deposited with the United States Postal Service, Express Mail Certificate No. EV149011806US, on this 20 day of December, 2002 in an envelope addressed to: Box DAC, Commissioner for Patents, Washington, DC 20231.


Leslie Ruano

PETITION TO ACCORD A FILING DATE PURSUANT TO 37 CFR §1.181

1. Applicants hereby petitions the Commissioner to accord a filing date pursuant to 37 CFR §1.181, 37 CFR §1.53, and MPEP 1001.02(b)(35).

2. On June 14, 2001, Applicants filed a COMPLETE patent application (copy attached hereto as Exhibit A) with the U.S. Patent and Trademark Office consisting of: sixteen (16) pages of specification including the Abstract; two (2) pages of claims; a copy of the priority document; six (6) pages of drawings and a Declaration and Power of Attorney. This application further included a self-addressed stamped postcard.

3. On June 14, 2001, this COMPLETE application, which included an executed Certificate of Express Mailing claiming a mailing date of June 14, 2001, was deposited with the U.S. Postal Service, Express Mail Label No. EL589447904US (copy attached hereto as Exhibit B).

4. On July 2, 2001, Applicants received back from the U.S. Patent and Trademark Office the self-addressed stamped postcard (copy attached hereto as Exhibit C) bearing the USPTO's mailroom stamp stating that on June 14, 2001, the U.S. Patent and Trademark Office received from Applicants a new patent application including: 16 pages of specification including Abstract; 2 pages of claims; 6 pages of drawings; Declaration and Power of Attorney; priority document; and a check for \$355.00.

12/24/2002 AUCNDALF1 00000064 09881478

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130.00 OP

Examination date: 01/03/2003 AKELLEY
12/24/2002 AUCNDALF1 00000064 09881478

-130.00 OP

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OFFICE OF PETITIONS

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Code/Number: 09881478 \$130.00 CR
12/24/2002 AUCNDALF1 00000064 09881478
01 FC:1460
Examination date: 01/03/2003 AKELLEY
12/24/2002 AUCNDALF1 00000064 09881478
-130.00 OP

5. On November 27, 2002, a NOTICE OF INCOMPLETE NONPROVISIONAL APPLICATION was mailed to the applicant (copy attached hereto as Exhibit D) stating that the application was deposited without drawings.

6. Applicants respectfully assert that the drawings were in fact deposited with the U.S. Patent and Trademark Office on June 14, 2001. Further, the U.S. Patent and Trademark Office acknowledged receipt of the same COMPLETE application via the return of the Applicants' self-addressed stamped postcard, which clearly indicated six (6) sheets of drawings were submitted with the application. Applicants respectfully assert that portions of the COMPLETE application were misplaced by personnel of the U.S. Patent and Trademark Office once within the confines of the U.S. Patent and Trademark Office.

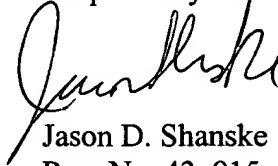
7. Accordingly, Applicants respectfully request that the Commissioner accept this Petition to Accord a Filing Date and accord a filing date of June 14, 2001, to the subject application.

8. A check in the amount of \$130.00 is enclosed. Since Applicants believe that this failure to accord a filing date was due to a mistake by the U.S. Patent and Trademark Office, Applicants respectfully request that the U.S. Patent and Trademark Office refund the \$130.00 petition fee by crediting Deposit Account No. 09-0002.

If any payment during prosecution is found to be incorrect, please charge any deficiency or credit any overpayment to my deposit account No. 09-0002.

If for any reason this petition is found to be incomplete or if at any time it appears that a telephone conference with counsel would help advance prosecution, please telephone the undersigned or his associate, Joseph S. Iandiorio, collect in Waltham, Massachusetts (781) 890-5678.

Respectfully submitted,


Jason D. Shanske
Reg. No. 43, 915

JDS:lr

Enclosures: Exhibits A-D

EXHIBIT A



INVESTOR IN PEOPLE

The Patent Office
Concept House
Cardiff Road
Newport
South Wales
NP10 8QQ

The undersigned, being an officer duly authorised in accordance with Section 74(1) and (4) of the Deregulation & Contracting Out Act 1994, to sign and issue certificates on behalf of the Controller-General, hereby certify that annexed hereto is a true copy of the documents as originally filed in connection with the patent application identified therein.

In accordance with the Patents (Companies Re-registration) Rules 1982, if a company named in this certificate and any accompanying documents has re-registered under the Companies Act 1980 with the same name as that with which it was registered immediately before re-registration save for the substitution as, or inclusion as, the last part of the name of the words "public limited company" or their equivalents in Welsh, references to the name of the company in this certificate and any accompanying documents shall be treated as references to the name with which it is so re-registered.

In accordance with the rules, the words "public limited company" may be replaced by p.l.c., plc, L.C. or PLC.

Re-registration under the Companies Act does not constitute a new legal entity but merely subjects the company to certain additional company law rules.

Signed

Dated

24 MAY 2001

15 JUN 2000

Office

Statement of inventorship and of right to grant of a patent

The Patent Office

Cardiff Road
Newport
Gwent NP9 1RH

1. Your reference

Jg-2614

2. Patent application number
(if you know it)

0014671.2

15 JUN 2000

3. Full name of the or of each applicant

SEOS DISPLAYS LIMITED

4. Title of the invention

HEAD SLAVED AREA OF INTEREST (HSAOI)
USING FRAMESTORE DEMULTIPLEXING

5. State how the applicant(s) derived the right
from the inventor(s) to be granted a patent

BY VIRTUE OF AGREEMENT AND
CONDITIONS OF EMPLOYMENT

6. How many, if any, additional Patents Forms
7/77 are attached to this form?
(see note (c))

2

7.

I/We believe that the person(s) named over the page (and on
any extra copies of this form) is/are the inventor(s) of the invention
which the above patent application relates to.

Signature

GRAHAM JONES & COMPANY

Date 15/6/00

8. Name and daytime telephone number of
person to contact in the United Kingdom

GRAHAM H. JONES
EUROPEAN PATENT ATTORNEY
CHARTERED PATENT ATTORNEY
TRADE MARK ATTORNEY

020 8858 4039

Notes

- If you need help to fill in this form or you have any questions, please contact the Patent Office on 0645 500505.
- Write your answers in capital letters using black ink or you may type them.
- If there are more than three inventors, please write the names and addresses of the other inventors on the back of another Patents Form 7/77 and attach it to this form.
- When an application does not declare any priority, or declares priority from an earlier UK application, you must provide enough copies of this form so that the Patent Office can send one to each inventor who is not an applicant.
- Once you have filled in the form you must remember to sign and date it.

Patents Form 7/77

COMBINED DECLARATION AND POWER OF ATTORNEY
IN ORIGINAL APPLICATION

Attorney Docket No.
GJ-223J

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; that

I verily believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named below) of the invention entitled: APPARATUS USING

FRAMESTORE DEMULTIPLEXING described and claimed in the attached specification, that I understand the content of the attached specification, including the claims, that I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months prior to this application, that I acknowledge my duty to disclose information of which I am aware which is known to be material to patentability in accordance with 37 CFR 1.56, and that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration, and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to this application by me or my legal representatives or assigns, except as follows:
UNITED KINGDOM PATENT APPLICATION No. 0014671.2 Filed 15th June 2000.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Joseph S. Iandiorio Kirk Teska

Jason D. Shanske

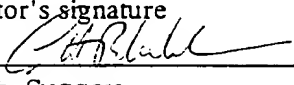
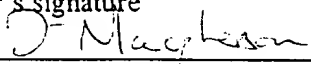
Reg. No. 23,095 Reg. No. 36,291

Reg. No. 43,915

Address all telephone calls to Joseph S. Iandiorio or Kirk Teska at (781) 890-5678.

Address all correspondence to Iandiorio & Teska, 260 Bear Hill Road, Waltham, MA 02451-1018.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of 1st or sole inventor GEOFFREY HOWARD BLACKHAM	Inventor's signature 	Date 10.5.01
Residence 58 Adastra Avenue, Keymer, West Sussex, BN6 8DR, United Kingdom.		Citizenship United Kingdom
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Full name of 2nd inventor IAN MURDOCH MACPHERSON	Inventor's signature 	Date 10.5.01
Residence 9 Mayfield Court, Junction Road, Burgess Hill, 12 Coombe Road, Steyning, West Sussex, BN44 3LF, United Kingdom.		Citizenship United Kingdom
Post office address 9 Mayfield Court, Junction Road, Burgess Hill, West Sussex, BN44 3LF, 12 Coombe Road, Steyning, West Sussex, BN44 3LF, United Kingdom.		
Full name of 3rd inventor	Inventor's signature	Date
Residence	Citizenship	
Post office address		

UNITED STATES OF AMERICA

SPECIFICATION

BE IT KNOWN that we Geoffrey Howard Blackham of 58 Adastra Avenue, Keymer, West Sussex, BN6 8DR, United Kingdom, a citizen of the United Kingdom, and Ian Murdoch Macpherson of 12 Coombe Road, Steyning, West Sussex, BN44 3LF, United Kingdom, a citizen of the United Kingdom, have invented new and useful APPARATUS USING FRAMESTORE DEMULTIPLEXING of which the following is a specification.

ABSTRACT

APPARATUS USING FRAMESTORE DEMULTIPLEXING

Apparatus comprising framestore demultiplexing means (32) for creating a specific area of interest which has a higher image update or frame rate than a surrounding area (Figure 7). The apparatus may be used in simulators such as flight simulators, air traffic control simulators, and driving simulators.

APPARATUS USING FRAMESTORE DEMULTIPLEXING

This invention relates to apparatus using framestore demultiplexing. The apparatus may be used in display apparatus, which display apparatus may itself be used in simulators such for example as flight simulators, traffic control simulators and driving simulators.

Display systems are known which utilise head slaved tracker means for a user's head, and in which a projected image is projected onto a spherical screen. A single image or two images may be slaved around inside a dome with the advantage that only one or two image generator channels are required to cover the field of view of the user. The known display systems have the following disadvantages.

- A steering system which is employed in the visual display systems has to respond very quickly to head movement (low latency).
- The steering system must have a high acceleration and velocity in order to match the rate of movement of the user's head.
- The high resolution field of view is quite small and the eyes of the user can see all around into a low resolution background image, since the high resolution field of view is not eye tracked.
- There is no imagery outside a typical $170^{\circ} \times 160^{\circ}$ low resolution background image, but the field of view of a human user is larger and is around 200° wide, so

that the user receives an impression of moving down a dark tunnel.

- The amount of light able to be sent through the optics in the known display systems is limited.

It is an aim of the present invention to obviate or reduce the above mentioned problems.

Accordingly, in one non-limiting embodiment of the present invention there is provided apparatus comprising framestore demultiplexing means for creating a specific area of interest which has a higher image update or frame rate than a surrounding area.

The apparatus may include tracker means for use with the framestore demultiplexing means. The tracker means may be a head slaved tracker means. The head slaved tracker means may provide a full update rate directly ahead of the user's gaze. The tracker means may alternatively be for tracking the specific area of interest.

The apparatus may include a high speed video switcher for use with the framestore demultiplexing means in order to create the specific area of interest.

The apparatus may include a video switch that selects data at sub-frame level from image sources that may update at different but synchronous rates.

The present invention also extends to display apparatus when including the apparatus comprising the framestore demultiplexing means.

The display apparatus may be large area display apparatus.

The invention still further provides a simulator when including the display apparatus. The simulator may be a flight simulator, an air traffic control simulator, a driving simulator, or any other suitable and appropriate desired simulator.

The display apparatus may utilise a large array tiled projection system which achieves a very high resolution throughout the field of view of the user. For example, the large array tiled projection system may comprise 60 separate high resolution displays. The large array tiled projection system gives a large number of channels which provides very high resolution imagery everywhere in the field of view of the user, approaching or exceeding eye limiting resolution. This is essential in certain training tasks, for example spotting aircraft orientation and type at realistic distant in an air traffic control simulator.

The number of channels in the display apparatus cannot easily be driven with a relatively low cost graphics computer. In view of this, the apparatus of the present invention may be configured to take a lesser quantity of images, for example 20, and spread the images out over a larger number of channels of display, for example 60 channels of display. This is done by interleaving images in time so that, for example, the graphics computer may output 60 new images per second on each of the 20 images,

with different views on alternate frames. High speed, real time image processing electronic means that merges high resolution and low resolution imagery may then be instructed to freeze/unfreeze the appropriate channel, which provides two discrete images, at 30Hz each from a single 60Hz source. This can be extended 4 x 15Hz images, or 8 x 7.5Hz images etc from a single channel. In this example, head tracking may be employed so that the full update rate is directly ahead of the gaze of the user, surrounded by a 30Hz zone, then 15Hz outside of that, and so on. Since the low update rate is in the periphery of the user's gaze, the low update rate is not distracting.

Embodiments of the invention will now be described solely by way of example and with reference to the accompanying drawings in which:

Figure 1 shows the imagery obtained from a known head slaved area of interest display apparatus;

Figure 2 shows a multiple channel multiple liquid crystal device tiled back projection sphere;

Figures 3a, 3b and 3c illustrate the operation of the apparatus of the present invention;

Figures 4, 5 and 6 show zoned areas and differences in update rate represented by the degree of darkness in the various areas;

Figure 7 illustrates display apparatus of the present invention;

Figure 8 illustrates how specific areas of interest at one frame rate with background images at a different frame rate are able to be composited together; and

Figure 9 shows display apparatus using a head slaved area of interest tracker means with the area of interest being at a higher frame rate than the display area surrounding the area of interest.

Referring to Figure 1, there is shown the imagery obtained from a known head slaved area of interest display system. The display system slaves two images around inside a dome. A single image may alternatively be slaved around inside the dome. The benefit of the known display system is that the display system only requires one or two image generator channels to cover the field of view of the user. As can be seen from Figure 1, the high resolution field of view 2 is quite small. More specifically, the high resolution field of view 2 gives a high resolution background image which is 40° wide x 30° tall. There is a low resolution background image 4 which is 170° wide x 160° tall. There is no imagery in the remaining field of view as indicated by the arrow 6. Because the human field of view is larger than 200° wide, there is an impression of moving down a dark tunnel. Because the high resolution field of view 2 is quite small, the eyes of the user can see all around into the low resolution background image 4. The steering mechanism has to respond very quickly to head movement, and the steering mechanism must have a high

acceleration and velocity to match the movement rates of the user's head. Still further, the amount of light able to be sent through the optics used in the known display system is limited.

Referring now to Figure 2, there is shown a 60 channel multiple liquid crystal display tiled back projection spherical display apparatus 8. The display apparatus 8 comprises projectors 10 mounted on a support dome 12. The projectors 10 are housed in projector housings 14. Inside the display apparatus 8 is a rear projection screen 16. A design eyepoint 18 is also shown.

The sphere 8 may operate with more or less than 60 channels. The large number of channels guarantees that high display resolution is available wherever the user looks. The resolution will ultimately be eye limiting, so that the display apparatus is as resolute as the human eye.

A main problem with a 60 channel display system, is the type of computer required to drive 60 channels of imagery. At today's cost, a 30 pipe Silicon Graphics Onyx image generator (which can produce 60 images of 1280 x 1024) would cost more than £15 million, and so this solution is not commercially feasible. A solution would be to use low cost personal computer based image generators, one per channel. This may be a feasible solution in the future when the low cost personal computer based image generators improve in quality, but today's personal computer based image generators do not approach the

performance of the above mentioned Onyx type of image generator.

The present invention utilises the realisation that high quality image generators, such for example as the above mentioned Onyx image generator, requires a way of putting a useful amount of high resolution imagery where needed, and a lower quality information elsewhere in the field of view of the user. By utilising tracker means, for example head tracker means, it is possible to utilise the fact that the images outside the human field of view are not required since the user cannot see them. It is possible to use the information that the human visual response biases the eye's highest resolution in a small area in the middle of the field of view. With a perfect eye tracking system, a display could be utilised covering only this small area, for example up to 10° , with high resolution imagery, but this is difficult to do and typically results in an artefact of appearing to view the image through a drinking straw. The use of head tracker means is preferred since it enables the coverage of a much larger area with high resolution imagery, so that the eyes can roam around and still see high resolution without such an artefact.

In the present invention, a certain amount of channels from a high performance image generator are used in order to provide a high resolution, full update rate (typically 60Hz) area which is head slaved. Outside of this high

resolution, high update rate area, there is a medium update area (for example 30Hz), and outside of this is low update rate imagery (for example 15Hz). By using a framestore on each display channel, the image generator is able to be controlled to interleave different channel imagery on the same video output. This video output is sent to multiple display channels, which are asked to freeze/unfreeze in order to let the relevant image through to the visual display.

In an example of the invention with reference to Figures 3a, 3b and 3c, ten pipes of a high quality image generator can be taken, with each pipe being able to generate $2 \times 1280 \times 1024$ images at 60Hz. Head tracker means feeds steering information to a real-time application running on the high quality image generator, which contains a software application which knows where the display channels are relative to the eye. This software application then controls a video switcher, which can switch any input channel to any output channel in real-time.

Six channels running at 60Hz covering the area straight ahead of the user's gaze are required. The software application configures the video switcher accordingly. The six channels are shown as the darkest zone in Figure 4.

The next zone that is required is the zone surrounding the darkest zone as shown in Figure 4. This next medium

update-rate zone comprises 14 channels which can be allowed to work at 30Hz. To do this, the software application is required to interleave two channel's display angles from each associated image generator channel. In this way, the temporal frame interleave arrangement shown at the top of Figure 3 is achieved.

By way of illustration, Figure 3a depicts how one image generator channel can interleave a 60Hz input to provide two 30Hz outputs using a framestore demultiplexer. The video switcher is used to feed the source image to both channel 1 and channel 2, and to send a synchronisation signal from a synchronisation processor 20 as shown in Figure 3c. This freezes the framestore of channel 2 on frame 1, and unfreezes the framestore on channel 2, then a freeze on frame 2, a freeze on the framestore of channel 1 and unfreeze channel 2, and so on. In this way, a single video output, running at 60Hz, is being split out into two 30Hz images. This resolution is maintained at 1280 x 1024, and simply the frame rate is dropped.

Finally, the outer zone depicted in Figure 4 with light shading, comprising 24 channels, can be allowed to display at an update rate of 15Hz. For this zone, the temporal interleave now operates as shown in Figure 3b on a one-to-four ratio, but is otherwise the same principle as for the medium update-rate zone.

Referring to Figure 3c, a 20 channel high quality image generator has 20 images, which are then sent to the

video switcher, which then goes to the 60 channels of the real time image distortion electronics present on the input to the 60 projectors.

Figure 4 shows the 60 channels (one small square each) of a rear projected ultra-high resolution display system comprising 60 projected images onto a small sphere. The darkest area is at 60Hz, the next darkest is at 30Hz, the light grey is at 15Hz, and the white squares have no image. The user's gaze is tracked so that the centre of the 60Hz area is always in front. What this accomplishes is exploitation of redundant image generator update rate capability to allow the use of less image generator channels. Normally 60 image generator channels would be required to drive 60 display channels, but in the case above, only 20 are needed, which is much more affordable. Thus the novel aspects are as follows:

- Using a framestore system to implement zoned areas outside the primary view direction to have a higher number of display channels than source channels updating the respective images at a correspondingly lower rate.
- Tracking the above area with head tracker means.
- Using a high speed video switcher to implement the movement of channels.
- Grouping the 15Hz channels together such that adjacent 15Hz channels are synchronised to minimise visual artefacts causing latency.

Assuming the use of a 60 channel rear-projected ultra-high resolution display system comprising 60 projected images onto a small sphere, the apparatus could alternatively be as follows:

- An image generator with n channels.
- A high speed video switcher with n input channels, 60 output channels.
- A synchronisation processor.
- 60 real-time image framestore units attached to 60 projectors (not necessarily liquid crystal display projectors). These framestore units may also include a distortion correction function to correct for projection onto the curved surface.
- A head tracker.
- A real-time software application which takes the head tracker input, and decides which channels are covered by zone A, B and C, and then configures the switcher and synchronisation processor accordingly.

Other zone sizes can be implemented as shown in Figures 5 and 6.

The video switcher, synchronisation processor and framestore units combine to form a real-time image processing system that processes whole frame units to interleave source channels to output channels.

The above described apparatus of the present invention addresses a lot of the existing problems with known head slaved area of interest systems. More specifically, with

the apparatus of the present invention, the following advantages may be obtained:

- The high update area is much larger.
- The resolution in the surrounding zones is still very high, and only the frame rate has dropped.
- There are no expensive moving parts to move the imagery around.
- The brightness is very high since there are many projectors.
- The entire human field of view is covered by imagery.

The above mentioned rear-projected ultra-high resolution display system has only one place to view the image that is correct. This is the centre of the spherical rear-projection screen 16 as shown in Figure 2 and it is known as the design eye point 18. The above mentioned field of view is the viewable area defined as angles measured from the user's eyes. The above mentioned image generator may be a graphics super-computer which can generate multiple images of a virtual world in real time so that the user can interact with a three dimensional world, for example by flying an aircraft around that world. The head slaved area of interest referred to above may be regarded as a head tracked high fidelity region in a larger display system.

Referring now to Figure 7, there is shown apparatus 22 comprising a section 24 providing computer generated images, a section 26 providing high speed, real-time image

processing electronics, and a section 28 providing a display system in the form of the sphere 8 and associated parts as shown in detail in Figure 2. The computer generated images are shown as background images 30 and specific area of interest (target) images 32. In section 26, the high speed, real-time image processing electronics is shown as apparatus 32 which processes and combines background and target (inset) images to produce a composite image. The composite image is then fed as shown to the apparatus forming the display system in section 28.

Figure 8 is similar to Figure 7 but shows a specific example as will be seen. In Figure 8, the section 28 has a display system comprising four output channels as shown from combined background, and target channels running at different frame rates. In this example, the target images are partical frame segments and the image processing electronics may insert any one or more of these into the scenes provided by the background channels. Hence at the video signal to any one display channel, there may be image data that updates at varying rates where part or parts of the image is updated at a higher rate than the remainder. The video switching function therefore operates at sub-frame level, where individual display image data is selected from sources that may update at different but synchronous rates.

The tracking of the areas of interest in this example is not achieved using a head tracking device. Instead, the simulator host computer system keeps track of the location of all such areas and supplies such tracking data to the real-time software in the image processing system. The software then provides switching data to the video switching function.

Figure 9 shows an example of apparatus of the invention which is a head slaved area of interest implementation with the area of interest being at a higher frame rate than the display area surrounding the area of interest. In the display system section 28, the channels designated as in the area of interest are at a higher frame rate than those surrounding the area of interest.

It is to be appreciated that the embodiments of the invention described above with reference to the drawings have been given by way of example only and that modifications may be effected.

CLAIMS

1. Apparatus comprising framestore demultiplexing means for creating a specific area of interest which has a higher frame rate than a surrounding area.
2. Apparatus according to claim 1 and including tracker means for use with the framestore demultiplexing means.
3. Apparatus according to claim 2 in which the tracker means is a head slaved tracker means.
4. Apparatus according to claim 2 and in which the tracker means is for tracking the specific area of interest.
5. Apparatus according to claim 1 and including a high speed video switcher for use with the framestore demultiplexing means in order to create the specific area of interest.
6. Apparatus according to claim 1 and including a video switch that selects data at sub-frame level from image sources that may update at different but synchronous rates.
7. Display apparatus comprising apparatus according to claim 1.

8. Display apparatus according to claim 7 and which is a large array display apparatus.

9. A simulator comprising display apparatus according to claim 8.

10. A simulator according to claim 9 in which the simulator is a flight simulator, an air traffic control simulator, or a driving simulator.

The Patent Office date stamp sets forth the date of receipt of:

Applicant: Blackham et al.

Serial No:

Title: APPARATUS USING FRAMESTORE
DEMULTIPLEXING

Docket No: GJ-223J

Atty's initials: JDS/lr

Enclosed:

Transmittal letter and copy;
New patent application including:
16 pgs. Specification including Abstract; 2 pgs.
claims; 6 pgs. drawings; Declaration and Power of
Attorney; Priority Document; and Check for \$355.00.

Express Mail Cert. EL589447904US

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Three Hundred Fifty-Five and 00/100*****

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Washington, DC 20231

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included.
Details on back.

MEMO GJ-223J Patent Appl.

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70705994673911

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1/6

Typical HSAG

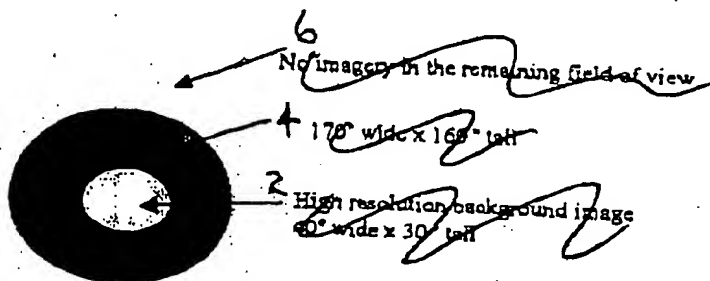
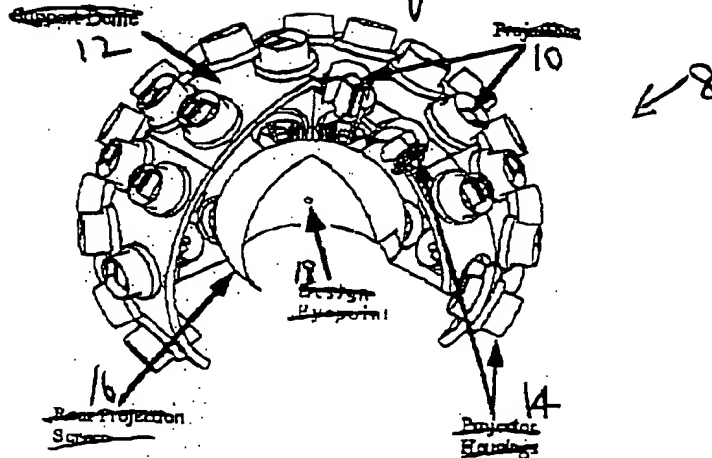


FIG 1

Remove arrow heads.
Only 8" has arrow head.



Example Display System

Figure X2

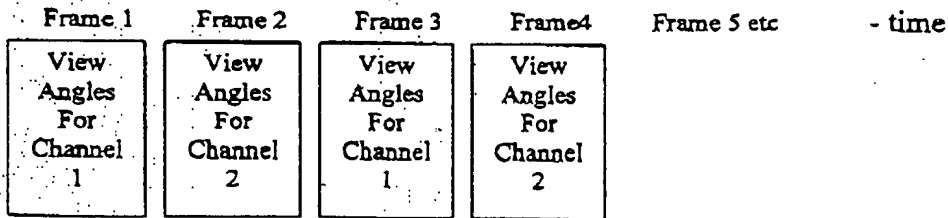


FIG. 3a

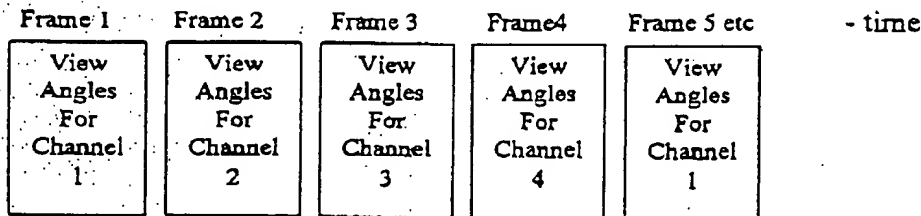


FIG. 3b

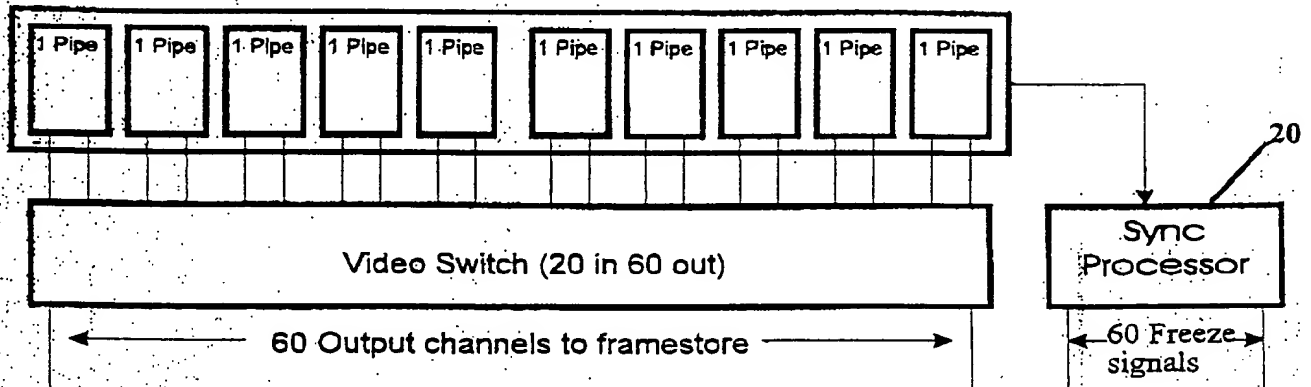


FIG. 3c



Fig 4

= 90° x 40° @ 60Hz
 = 150° x 80° @ 30Hz
 = 270° x 100° @ 15Hz



Fig 5

= 60° x 60° @ 60Hz
 = 120° x 100° @ 30Hz
 = 210° x 100° @ 15Hz



Fig 6

= 60° x 40° @ 60Hz
 = 180° x 80° @ 30Hz
 = 240° x 100° @ 15Hz

4/6

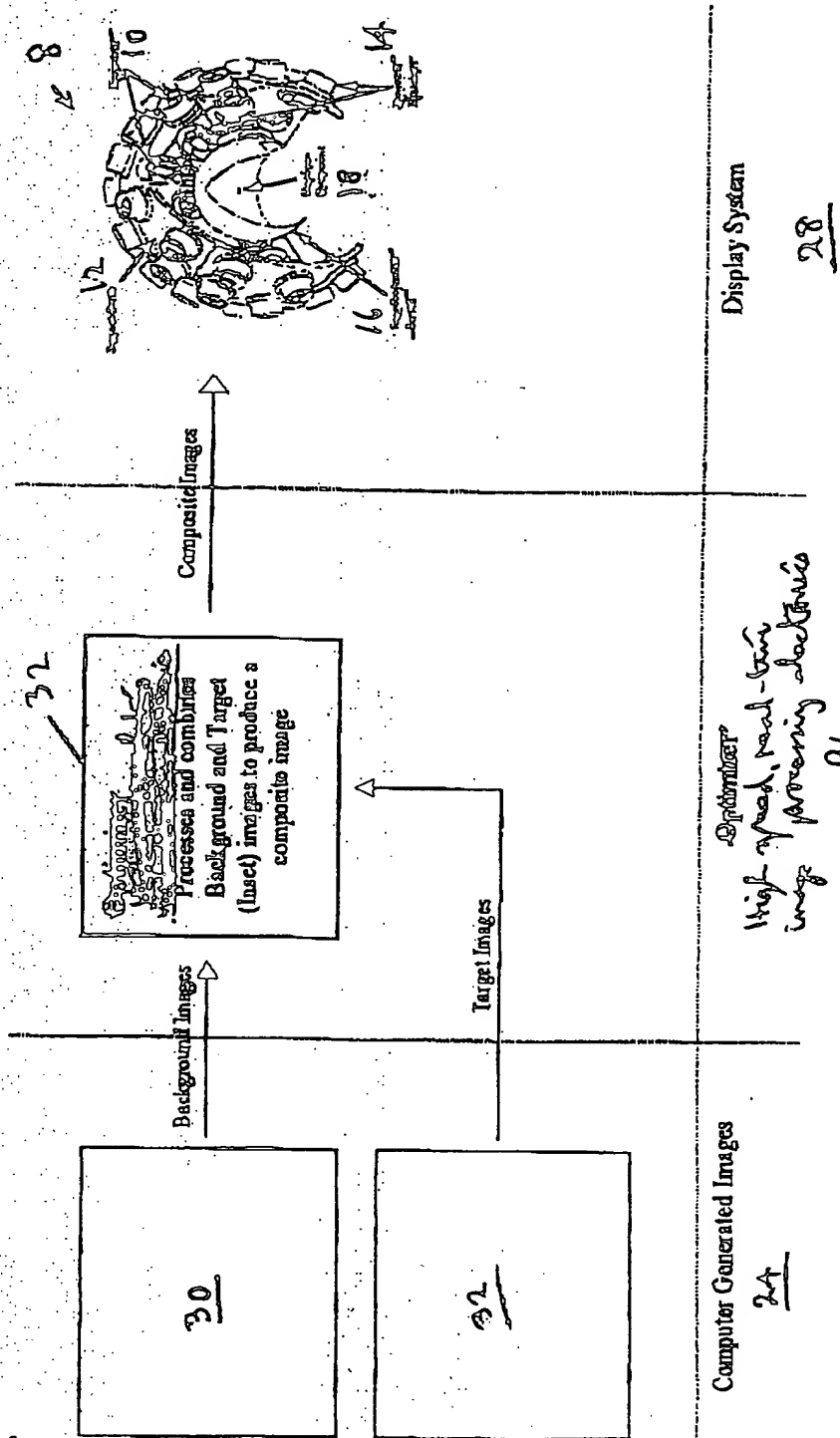


Figure 17

Example Block Diagram showing how Optimizer fits into the Visual Display System.

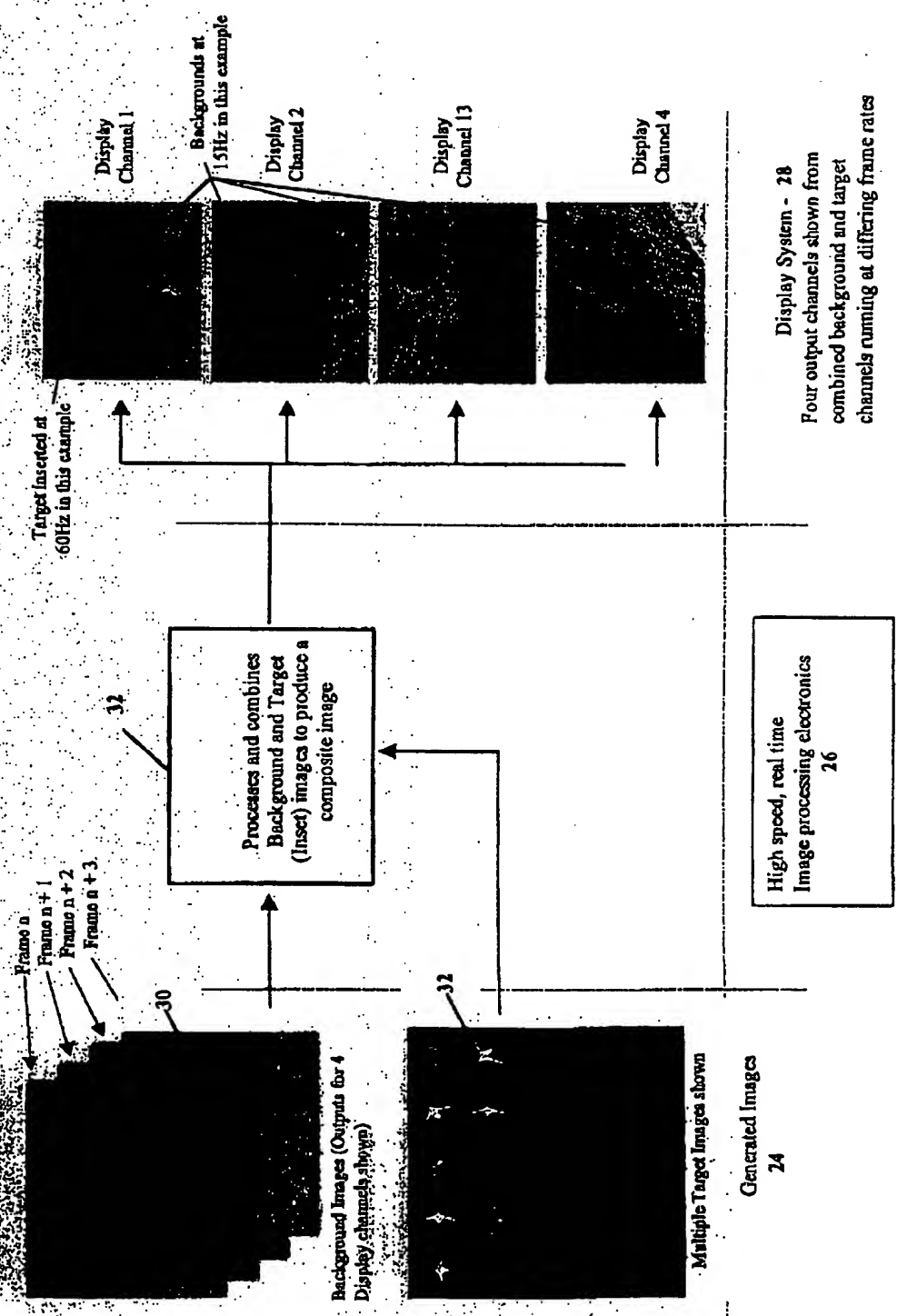


Figure 8

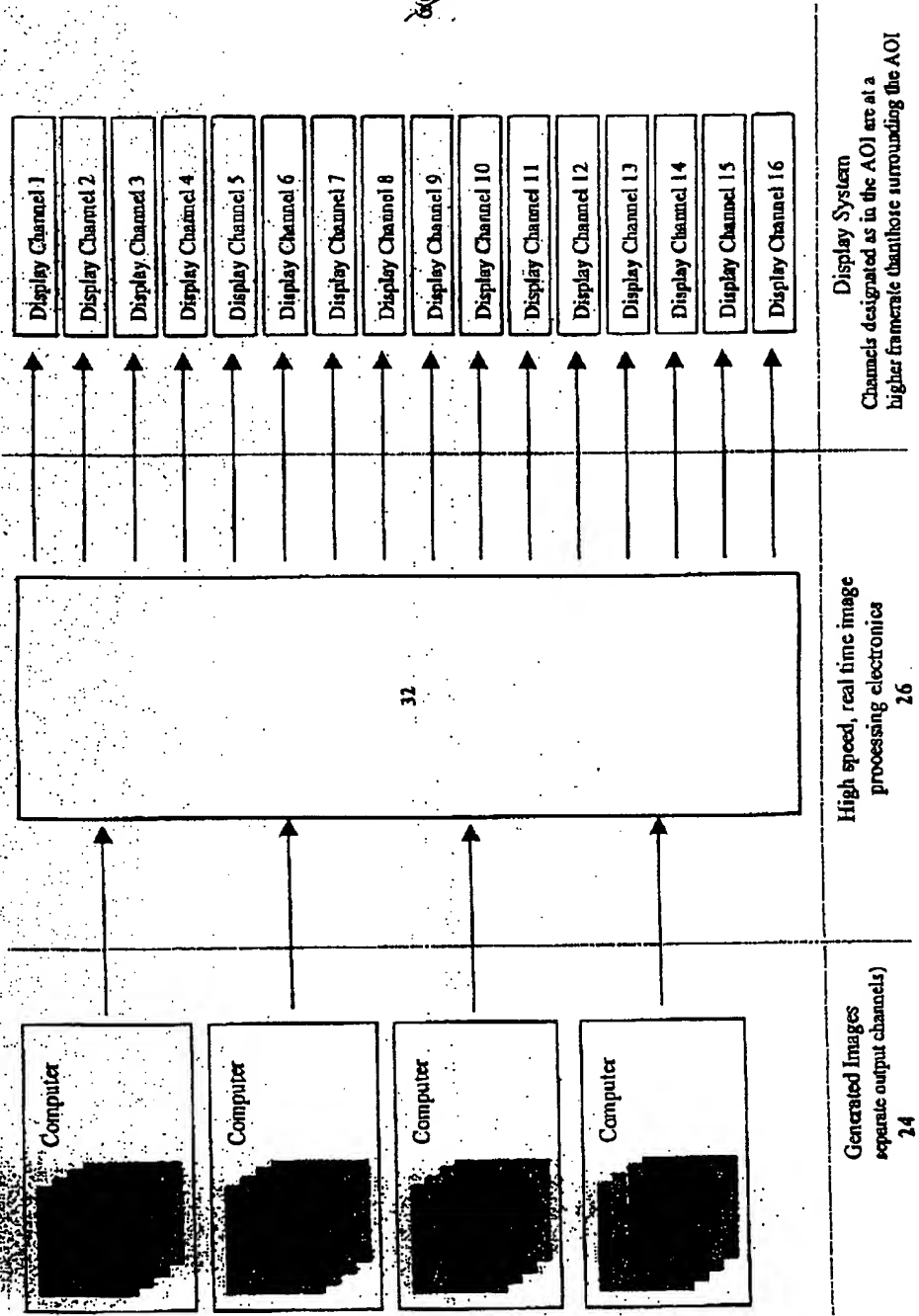
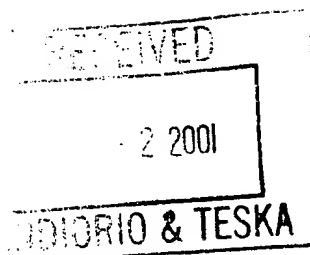


Figure 9

EXHIBIT B



The Patent Office date stamp sets forth the date of receipt of:

Applicant: Blackham et al.

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APPARATUS USING FRAMESTORE
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Docket No:

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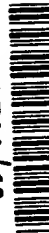
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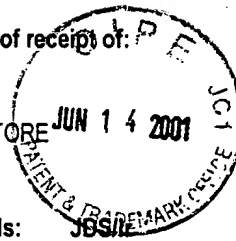
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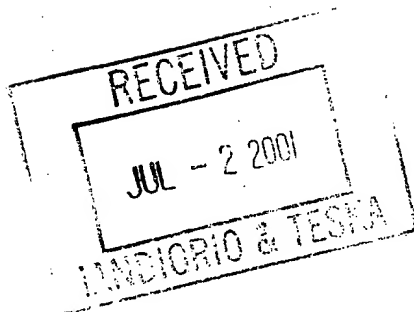
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web: www.iandiorio.com

December 20, 2002

Box DAC
Commissioner for Patents
Washington, DC 20231

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SUBJECT: Applicant: Blackham et al.
 Serial No: 09/881,478
 Filed: June 14, 2001
 For: APPARATUS USING FRAMESTORE
 DEMULTIPLEXY
 Docket No: GJ-223J

Dear Sir:

Enclosed is a Petition to Accord a Filing Date Pursuant to 37 CFR § 1.181 for the subject application. Also enclosed are Exhibits A-D and a check in the amount of \$130.00 for the filing fee.

If for any reason this Petition is found to be INCOMPLETE, or if at any time it appears that a TELEPHONE CONFERENCE with counsel would help advance prosecution, please telephone the undersigned or his associates, collect in Waltham, Massachusetts (781) 890-5678.

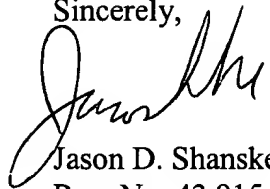
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In addition, pursuant to Rule 1.136(a)(3), the Office is hereby authorized to treat any future reply requiring an extension of time as incorporating a request therefor. Also, any request or Petition for an Extension of Time notwithstanding an inadvertent reference in the Petition to a shorter period of time is to be treated as requesting the appropriate length of time.

Commissioner for Patents
December 20, 2002
Page 2

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Jason D. Shanske
Reg. No. 43,915

JDS:lr
Enclosures



IANDIORIO & TESKA

INTELLECTUAL PROPERTY LAW ATTORNEYS

Joseph S. Iandiorio
Kirk Teska
Jason D. Shanske
Thomas E. Thompkins, Jr.
Roy J. Coleman
R. Stephen Rosenholm
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SUBJECT: Applicant: Blackham et al.
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DEMULTIPLEX
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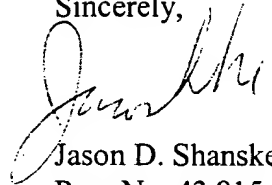
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June 14, 2001

Box Patent Application
Assistant Commissioner of Patents
Washington, DC 20231

SUBJECT: APPARATUS USING FRAMESTORE DEMULTIPLEXING
GJ-223J

Dear Sir:

Enclosed is a patent application including formal papers as follows:

Applicant: Blackham et al.

Title: APPARATUS USING FRAMESTORE DEMULTIPLEXING

No. Pages Specification including Abstract: 16; Claims: 2; Drawings: 6; Priority Document; and Combined Declaration and Power of Attorney.

Filing Fee Calculation

Basic Fee: \$ 355.00

Additional Fees:

Total number of claims in excess
of 20: 0 x \$9.00 \$.00

Number of independent claims in
excess of 3: 0 x \$40.00 \$.00

TOTAL FILING FEE: \$ 355.00

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EL589447904US

June 14, 2001

Page 2

Applicant hereby claims priority under 35 USC 119, 120, 363, 365, and 37 CFR 1.55, based on United Kingdom Application No. 0014671.2, filed on June 15, 2000.

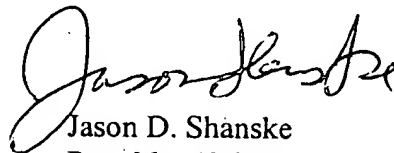
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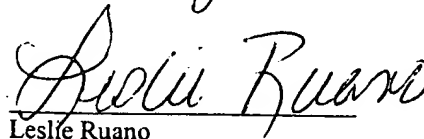


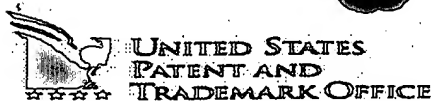
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APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/881,478	06/14/2001	Geoffrey Howard Blackham	GJ-223J

Iandiorio & Teska
260 Bear Hill Road
Waltham, MA 02451-1018



CONFIRMATION NO. 7893

FORMALITIES LETTER



OC000000009166769

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A filing date has NOT been accorded to the above-identified application papers for the reason(s) indicated below.

All of the items noted below and a newly executed oath or declaration covering the items must be submitted within **TWO MONTHS** of the date of this Notice, unless otherwise indicated, or proceedings on the application will be terminated (37 CFR 1.53(e)).

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- o The application was deposited without drawings. 35 U.S.C. 113 (first sentence) requires a drawing "where necessary for the understanding of the subject matter sought to be patented." *Applicant should reconsider whether the drawings are necessary under 35 U.S.C. 113 (first sentence).*

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